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## BIB DATA SHEET

CONFIRMATION NO. 7171

SERIAL NUMBER	FILING or 371(c) DATE RULE	CLASS	GROUP ART UNIT	ATTORNEY DOCKET NO.
10/582,711		438	2892	4590-523

## APPLICANTS

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## \*\* CONTINUING DATA \*\*\*\*\*

This application is a 371 of PCT/EP04/53003 11/18/2004

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

FRANCE 03/14595 12/12/2003

## \*\* IF REQUIRED, FOREIGN FILING LICENSE GRANTED \*\*

03/23/2007

Foreign Priority claimed	<input checked="" type="checkbox"/> Yes <input type="checkbox"/> No	STATE OR COUNTRY	SHEETS DRAWINGS	TOTAL CLAIMS	INDEPENDENT CLAIMS
35 USC 119(a-d) conditions met	<input checked="" type="checkbox"/> Yes <input type="checkbox"/> No	<input type="checkbox"/> Met after Allowance			
Verified and Acknowledged	/ERIC W JONES/ Examiner's Signature	Initials	FRANCE	5	<input checked="" type="checkbox"/> 10 <input type="checkbox"/> 15 <input type="checkbox"/> 1 <input type="checkbox"/> 2

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## TITLE

Method for producing electronic chips consisting of thinned silicon

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